

PANIMALAR ENGINEERING COLLEGE

DEPARTMENT OF ECE

II YEAR / IV SEMESTER EVEN JAN-MAY 2025

PRACTICAL EXAMINATION SCHEDULE

23EC1411 – ELECTRONICS CIRCUITS AND SIMULATION LABORATORY-II

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

Total No. of Students: 504

VENUE I-CIRCUITS SIMULATION LAB-I (LAB BLOCK -II FIRST FLOOR)

S. No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	28/04/2025 MONDAY	FN	13	31	211423106385– 6405 211423106407– 6416 (Except - 211423106406)
		AN	15	31	211423106448– 6478
2.	29/04/2025 TUESDAY	FN	9	32	211423106259– 6290
		AN	11	31	211423106323– 6353
3.	30/04/2025 WEDNESDAY	FN	5	32	211423106131– 6162
		AN	7	32	211423106195– 6226
4.	02/05/2025 FRIDAY	FN	1	32	211423106001– 6032
		AN	3	32	211423106066– 6097


LAB IN-CHARGE


HOD/ECE
Dr. S. RAJAKUMAR, M.E., Ph.D.,
Professor and Head
Electronics and Communication Engineering
Panimalar Engineering College,
Nasarathpettai, Poonamallee,
Chennai-600 123.

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23EC1411 – ELECTRONICS CIRCUITS AND SIMULATION LABORATORY-II

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

VENUE II -Circuits Simulation Lab-II (LAB BLOCK -II FIRST FLOOR)

S. No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	28/04/2025 MONDAY	FN	14	31	211423106417– 6447
		AN	16	29	211423106479– 6496, 211423106498-6504, 211423106901– 6904 (Except - 211423106497)
2.	29/04/2025 TUESDAY	FN	10	32	211423106291– 6322
		AN	12	31	211423106354– 6384
3.	30/04/2025 WEDNESDAY	FN	6	32	211423106163– 6194
		AN	8	32	211423106227– 6258
4.	02/05/2025 FRIDAY	FN	2	32	211423106033– 6052 211423106054– 6065 (Except - 211423106053)
		AN	4	32	211423106098– 6125 211423106127– 6130 (Except - 211423106126)



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PRACTICAL EXAMINATION SCHEDULE

23EC1412 - ANALOG AND DIGITAL CIRCUITS LABORATORY-I

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

Total No. of Students: 504

VENUE 1 - ANALOG AND DIGITAL COMMUNICATION LAB-I (LAB BLOCK -II FIRST FLOOR)

S. No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	28/04/2025 MONDAY	FN	9	32	211423106259– 6290
		AN	11	31	211423106323– 6353
2.	29/04/2025 TUESDAY	FN	13	31	211423106385– 6405 211423106407– 6416 (Except - 211423106406)
		AN	15	31	211423106448– 6478
3.	30/04/2025 WEDNESDAY	FN	1	32	211423106001– 6032
		AN	3	32	211423106066– 6097
4.	02/05/2025 FRIDAY	FN	5	32	211423106131– 6162
		AN	7	32	211423106195– 6226


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II YEAR / IV SEMESTER EVEN JAN-MAY 2025

PRACTICAL EXAMINATION SCHEDULE

23EC1412 - ANALOG AND DIGITAL CIRCUITS LABORATORY-I

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

VENUE II - ANALOG AND DIGITAL COMMUNICATION LAB-II (LAB BLOCK -II SECOND FLOOR)

S. No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	28/04/2025 MONDAY	FN	10	32	211423106291– 6322
		AN	12	31	211423106354– 6384
2.	29/04/2025 TUESDAY	FN	14	31	211423106417– 6447
		AN	16	29	211423106479– 6496, 211423106498–6504, 211423106901– 6904 (Except - 211423106497)
3.	30/04/2025 WEDNESDAY	FN	2	32	211423106033– 6052 211423106054– 6065 (Except - 211423106053)
		AN	4	32	211423106098– 6125 211423106127– 6130 (Except - 211423106126)
4.	02/05/2025 FRIDAY	FN	6	32	211423106163– 6194
		AN	8	32	211423106227– 6258


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PANIMALAR ENGINEERING COLLEGE

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II YEAR / IV SEMESTER EVEN JAN-MAY 2025

PRACTICAL EXAMINATION SCHEDULE

23CS1412– PRINCIPLES OF DATA STRUCTURES LABORATORY

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

Total No. of Students: 504

VENUE 1 –CONTROL SYSTEM ENGINEERING LAB (LAB BLOCK -I SECOND FLOOR)

S. No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	28/04/2025 MONDAY	FN	1	32	211423106001– 6032
		AN	3	32	211423106066– 6097
2.	29/04/2025 TUESDAY	FN	5	32	211423106131– 6162
		AN	7	32	211423106195– 6226
3.	30/04/2025 WEDNESDAY	FN	9	32	211423106259– 6290
		AN	11	31	211423106323– 6353
4.	02/05/2025 FRIDAY	FN	13	31	211423106385– 6405 211423106407– 6416 (Except - 211423106406)
		AN	15	31	211423106448– 6478


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PRACTICAL EXAMINATION SCHEDULE

23CS1412- PRINCIPLES OF DATA STRUCTURES LABORATORY

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

VENUE 2 – AIML LAB (LAB BLOCK -III GROUND FLOOR)

S. No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	28/04/2025 MONDAY	FN	2	32	211423106033– 6052 211423106054– 6065 (Except - 211423106053)
		AN	4	32	211423106098– 6125 211423106127– 6130 (Except - 211423106126)
2.	29/04/2025 TUESDAY	FN	6	32	211423106163– 6194
		AN	8	32	211423106227– 6258
3.	30/04/2025 WEDNESDAY	FN	10	32	211423106291– 6322
		AN	12	31	211423106354– 6384
4.	02/05/2025 FRIDAY	FN	14	31	211423106417– 6447
		AN	16	29	211423106479– 6496, 211423106498–6504, 211423106901– 6904 (Except - 211423106497)


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DEPARTMENT OF ECE

II YEAR / IV SEMESTER EVEN JAN-MAY 2025

PRACTICAL ARREAR EXAMINATION SCHEDULE

23EC1311 – ELECTRONICS CIRCUITS AND SIMULATION LABORATORY-I

AN – 12.00 Noon to 3.00 PM

VENUE II -Circuits Simulation Lab-II (LAB BLOCK -II FIRST FLOOR)

S. No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	29/04/2025 TUESDAY	AN	-	1	211423106315


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PRACTICAL ARREAR EXAMINATION SCHEDULE

23EC1312 – CONTROL SYSTEMS ENGINEERING LABORATORY

FN – 8.00 AM to 11.00 AM

VENUE 1 - ANALOG AND DIGITAL COMMUNICATION LAB-I (LAB BLOCK -II FIRST FLOOR)

S. No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	30/04/2025 WEDNESDAY	FN	-	2	211423106119, 211423106904,


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